

Sr. No.	Student Name	College Rollno	Exam Rollno	Part	Sem	Paper Code	Paper Name	Max Marks	Obt. Marks	Signature
1	PRADYUMN YADAV	15/151303	15048567001	III	VI	32221601	ELECTROMAGNETIC THEORY	25	22	
	PRADYUMN YADAV	15/151303	15048567001	III	VI	32221602	STATISTICAL MECHANICS	25	24	
	PRADYUMN YADAV	15/151303	15048567001	III	VI	32227613	COMMUNICATION SYSTEM	25	24	
	PRADYUMN YADAV	15/151303	15048567001	III	VI	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	25	24	
2	PANKAJ SONI	15/151306	15048567002	III	VI	32221601	ELECTROMAGNETIC THEORY	25	16	
	PANKAJ SONI	15/151306	15048567002	III	VI	32221602	STATISTICAL MECHANICS	25	23	
	PANKAJ SONI	15/151306	15048567002	III	VI	32227613	COMMUNICATION SYSTEM	25	17	
	PANKAJ SONI	15/151306	15048567002	III	VI	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	25	20	
3	AKSHAY KUMAR	15/151315	15048567003	III	VI	32221601	ELECTROMAGNETIC THEORY	25	17	
	AKSHAY KUMAR	15/151315	15048567003	III	VI	32221602	STATISTICAL MECHANICS	25	13	
	AKSHAY KUMAR	15/151315	15048567003	III	VI	32227613	COMMUNICATION SYSTEM	25	19	
	AKSHAY KUMAR	15/151315	15048567003	III	VI	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	25	18	
4	HARSH KUSHWAHA	15/151318	15048567004	III	VI	32221601	ELECTROMAGNETIC THEORY	25	16	
	HARSH KUSHWAHA	15/151318	15048567004	III	VI	32221602	STATISTICAL MECHANICS	25	20	
	HARSH KUSHWAHA	15/151318	15048567004	III	VI	32227613	COMMUNICATION SYSTEM	25	16	
	HARSH KUSHWAHA	15/151318	15048567004	III	VI	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	25	19	
5	ASHISH	15/151319	15048567005	III	VI	32221601	ELECTROMAGNETIC THEORY	25	20	
	ASHISH	15/151319	15048567005	III	VI	32221602	STATISTICAL MECHANICS	25	24	
	ASHISH	15/151319	15048567005	III	VI	32227613	COMMUNICATION SYSTEM	25	24	
	ASHISH	15/151319	15048567005	III	VI	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	25	22	
6	AMAN PATEL	15/151321	15048567006	III	VI	32221601	ELECTROMAGNETIC THEORY	25	12	
	AMAN PATEL	15/151321	15048567006	III	VI	32221602	STATISTICAL MECHANICS	25	13	
	AMAN PATEL	15/151321	15048567006	III	VI	32227613	COMMUNICATION SYSTEM	25	15	
	AMAN PATEL	15/151321	15048567006	III	VI	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	25	15	
7	MANISH YADAV	15/151322	15048567007	III	VI	32221601	ELECTROMAGNETIC THEORY	25	18	
	MANISH YADAV	15/151322	15048567007	III	VI	32221602	STATISTICAL MECHANICS	25	24	
	MANISH YADAV	15/151322	15048567007	III	VI	32227613	COMMUNICATION SYSTEM	25	21	

	MANISH YADAV	15/151322	15048567007	III	VI	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	25	19
8	GARVIT DAGAR	15/151328	15048567008	III	VI	32221601	ELECTROMAGNETIC THEORY	25	21
	GARVIT DAGAR	15/151328	15048567008	III	VI	32221602	STATISTICAL MECHANICS	25	24
	GARVIT DAGAR	15/151328	15048567008	III	VI	32227613	COMMUNICATION SYSTEM	25	22
	GARVIT DAGAR	15/151328	15048567008	III	VI	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	25	17
9	RATNAKAR SINGH PRINCE	15/151335	15048567010	III	VI	32221601	ELECTROMAGNETIC THEORY	25	16
	RATNAKAR SINGH PRINCE	15/151335	15048567010	III	VI	32221602	STATISTICAL MECHANICS	25	13
	RATNAKAR SINGH PRINCE	15/151335	15048567010	III	VI	32227613	COMMUNICATION SYSTEM	25	20
	RATNAKAR SINGH PRINCE	15/151335	15048567010	III	VI	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	25	17
10	ABHISHEK OLA	15/151336	15048567011	III	VI	32221601	ELECTROMAGNETIC THEORY	25	23
	ABHISHEK OLA	15/151336	15048567011	III	VI	32221602	STATISTICAL MECHANICS	25	23
	ABHISHEK OLA	15/151336	15048567011	III	VI	32227613	COMMUNICATION SYSTEM	25	24
	ABHISHEK OLA	15/151336	15048567011	III	VI	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	25	24
11	RESHAV KUMAR	15/151337	15048567012	III	VI	32221601	ELECTROMAGNETIC THEORY	25	21
	RESHAV KUMAR	15/151337	15048567012	III	VI	32221602	STATISTICAL MECHANICS	25	21
	RESHAV KUMAR	15/151337	15048567012	III	VI	32227613	COMMUNICATION SYSTEM	25	24
	RESHAV KUMAR	15/151337	15048567012	III	VI	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	25	23
12	ABHISHEK YADAV	15/151340	15048567014	III	VI	32221601	ELECTROMAGNETIC THEORY	25	23
	ABHISHEK YADAV	15/151340	15048567014	III	VI	32221602	STATISTICAL MECHANICS	25	25
	ABHISHEK YADAV	15/151340	15048567014	III	VI	32227613	COMMUNICATION SYSTEM	25	24
	ABHISHEK YADAV	15/151340	15048567014	III	VI	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	25	25
13	SACHIN KUMAR PAL	15/151346	15048567017	III	VI	32221601	ELECTROMAGNETIC THEORY	25	15
	SACHIN KUMAR PAL	15/151346	15048567017	III	VI	32221602	STATISTICAL MECHANICS	25	18

	SACHIN KUMAR PAL	15/151346	15048567017	III	VI	32227613	COMMUNICATION SYSTEM	25	15
	SACHIN KUMAR PAL	15/151346	15048567017	III	VI	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	25	17
14	SHIVANAND YADAV	15/151347	15048567018	III	VI	32221601	ELECTROMAGNETIC THEORY	25	16
	SHIVANAND YADAV	15/151347	15048567018	III	VI	32221602	STATISTICAL MECHANICS	25	20
	SHIVANAND YADAV	15/151347	15048567018	III	VI	32227613	COMMUNICATION SYSTEM	25	19
	SHIVANAND YADAV	15/151347	15048567018	III	VI	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	25	20
15	SAPANA YADAV	15/151351	15048567019	III	VI	32221601	ELECTROMAGNETIC THEORY	25	25
	SAPANA YADAV	15/151351	15048567019	III	VI	32221602	STATISTICAL MECHANICS	25	23
	SAPANA YADAV	15/151351	15048567019	III	VI	32227613	COMMUNICATION SYSTEM	25	24
	SAPANA YADAV	15/151351	15048567019	III	VI	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	25	23
16	HARSHITA TIWARI	15/151352	15048567020	III	VI	32221601	ELECTROMAGNETIC THEORY	25	16
	HARSHITA TIWARI	15/151352	15048567020	III	VI	32221602	STATISTICAL MECHANICS	25	18
	HARSHITA TIWARI	15/151352	15048567020	III	VI	32227613	COMMUNICATION SYSTEM	25	18
	HARSHITA TIWARI	15/151352	15048567020	III	VI	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	25	0
17	MANISH KUMAR AWANA	15/151353	15048567021	III	VI	32221601	ELECTROMAGNETIC THEORY	25	16
	MANISH KUMAR AWANA	15/151353	15048567021	III	VI	32221602	STATISTICAL MECHANICS	25	17
	MANISH KUMAR AWANA	15/151353	15048567021	III	VI	32227613	COMMUNICATION SYSTEM	25	18
	MANISH KUMAR AWANA	15/151353	15048567021	III	VI	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	25	18
18	DHANANJAY DWIVEDI	15/151359	15048567022	III	VI	32221601	ELECTROMAGNETIC THEORY	25	19
	DHANANJAY DWIVEDI	15/151359	15048567022	III	VI	32221602	STATISTICAL MECHANICS	25	22

	DHANANJAY DWIVEDI	15/151359	15048567022	III	VI	32227613	COMMUNICATION SYSTEM	25	23
	DHANANJAY DWIVEDI	15/151359	15048567022	III	VI	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	25	20
19	SHUBHAM KASHYAP	15/151360	15048567023	III	VI	32221601	ELECTROMAGNETIC THEORY	25	14
	SHUBHAM KASHYAP	15/151360	15048567023	III	VI	32221602	STATISTICAL MECHANICS	25	18
	SHUBHAM KASHYAP	15/151360	15048567023	III	VI	32227613	COMMUNICATION SYSTEM	25	19
	SHUBHAM KASHYAP	15/151360	15048567023	III	VI	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	25	20
20	ADITYA KUMAR	15/151361	15048567024	III	VI	32221601	ELECTROMAGNETIC THEORY	25	14
	ADITYA KUMAR	15/151361	15048567024	III	VI	32221602	STATISTICAL MECHANICS	25	13
	ADITYA KUMAR	15/151361	15048567024	III	VI	32227613	COMMUNICATION SYSTEM	25	16
	ADITYA KUMAR	15/151361	15048567024	III	VI	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	25	16
21	YOGESH KUMAR	15/151364	15048567025	III	VI	32221601	ELECTROMAGNETIC THEORY	25	18
	YOGESH KUMAR	15/151364	15048567025	III	VI	32221602	STATISTICAL MECHANICS	25	20
	YOGESH KUMAR	15/151364	15048567025	III	VI	32227613	COMMUNICATION SYSTEM	25	16
	YOGESH KUMAR	15/151364	15048567025	III	VI	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	25	17
22	BASANT KUMAR VERMA	15/151367	15048567026	III	VI	32221601	ELECTROMAGNETIC THEORY	25	18
	BASANT KUMAR VERMA	15/151367	15048567026	III	VI	32221602	STATISTICAL MECHANICS	25	20
	BASANT KUMAR VERMA	15/151367	15048567026	III	VI	32227613	COMMUNICATION SYSTEM	25	18
	BASANT KUMAR VERMA	15/151367	15048567026	III	VI	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	25	19
23	ANSHUMAN	15/151376	15048567027	III	VI	32221601	ELECTROMAGNETIC THEORY	25	13
	ANSHUMAN	15/151376	15048567027	III	VI	32221602	STATISTICAL MECHANICS	25	14
	ANSHUMAN	15/151376	15048567027	III	VI	32227613	COMMUNICATION SYSTEM	25	18
	ANSHUMAN	15/151376	15048567027	III	VI	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	25	19

24	VIKAS YADAV	15/151377	15048567028	III	VI	32221601	ELECTROMAGNETIC THEORY	25	15
	VIKAS YADAV	15/151377	15048567028	III	VI	32221602	STATISTICAL MECHANICS	25	16
	VIKAS YADAV	15/151377	15048567028	III	VI	32227613	COMMUNICATION SYSTEM	25	17
	VIKAS YADAV	15/151377	15048567028	III	VI	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	25	20
25	SUPRABHAT KUMAR	15/151380	15048567029	III	VI	32221601	ELECTROMAGNETIC THEORY	25	0
	SUPRABHAT KUMAR	15/151380	15048567029	III	VI	32221602	STATISTICAL MECHANICS	25	13
	SUPRABHAT KUMAR	15/151380	15048567029	III	VI	32227613	COMMUNICATION SYSTEM	25	0
	SUPRABHAT KUMAR	15/151380	15048567029	III	VI	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	25	0
26	ARVIND MEENA	15/151383	15048567030	III	VI	32221601	ELECTROMAGNETIC THEORY	25	15
	ARVIND MEENA	15/151383	15048567030	III	VI	32221602	STATISTICAL MECHANICS	25	19
	ARVIND MEENA	15/151383	15048567030	III	VI	32227613	COMMUNICATION SYSTEM	25	13
	ARVIND MEENA	15/151383	15048567030	III	VI	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	25	15
27	HARVENDRA SINGH	15/151385	15048567031	III	VI	32221601	ELECTROMAGNETIC THEORY	25	17
	HARVENDRA SINGH	15/151385	15048567031	III	VI	32221602	STATISTICAL MECHANICS	25	19
	HARVENDRA SINGH	15/151385	15048567031	III	VI	32227613	COMMUNICATION SYSTEM	25	19
	HARVENDRA SINGH	15/151385	15048567031	III	VI	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	25	18
28	RAJESH	15/151387	15048567032	III	VI	32221601	ELECTROMAGNETIC THEORY	25	18
	RAJESH	15/151387	15048567032	III	VI	32221602	STATISTICAL MECHANICS	25	23
	RAJESH	15/151387	15048567032	III	VI	32227613	COMMUNICATION SYSTEM	25	20
	RAJESH	15/151387	15048567032	III	VI	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	25	15
29	ANSHUMAN BHADARIA	15/151389	15048567033	III	VI	32221601	ELECTROMAGNETIC THEORY	25	19
	ANSHUMAN BHADARIA	15/151389	15048567033	III	VI	32221602	STATISTICAL MECHANICS	25	23

	ANSHUMAN BHADARIA	15/151389	15048567033	III	VI	32227613	COMMUNICATION SYSTEM	25	23
	ANSHUMAN BHADARIA	15/151389	15048567033	III	VI	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	25	21
30	EKTA CHAUHAN	15/151391	15048567034	III	VI	32221601	ELECTROMAGNETIC THEORY	25	19
	EKTA CHAUHAN	15/151391	15048567034	III	VI	32221602	STATISTICAL MECHANICS	25	20
	EKTA CHAUHAN	15/151391	15048567034	III	VI	32227613	COMMUNICATION SYSTEM	25	23
	EKTA CHAUHAN	15/151391	15048567034	III	VI	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	25	19
31	NIKHIL	15/151401	15048567036	III	VI	32221601	ELECTROMAGNETIC THEORY	25	22
	NIKHIL	15/151401	15048567036	III	VI	32221602	STATISTICAL MECHANICS	25	24
	NIKHIL	15/151401	15048567036	III	VI	32227613	COMMUNICATION SYSTEM	25	21
	NIKHIL	15/151401	15048567036	III	VI	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	25	18
32	ABHISHEK MALIK	15/151402	15048567037	III	VI	32221601	ELECTROMAGNETIC THEORY	25	23
	ABHISHEK MALIK	15/151402	15048567037	III	VI	32221602	STATISTICAL MECHANICS	25	23
	ABHISHEK MALIK	15/151402	15048567037	III	VI	32227613	COMMUNICATION SYSTEM	25	22
	ABHISHEK MALIK	15/151402	15048567037	III	VI	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	25	17
33	HIMANSHU SHARMA	15/151403	15048567038	III	VI	32221601	ELECTROMAGNETIC THEORY	25	22
	HIMANSHU SHARMA	15/151403	15048567038	III	VI	32221602	STATISTICAL MECHANICS	25	25
	HIMANSHU SHARMA	15/151403	15048567038	III	VI	32227613	COMMUNICATION SYSTEM	25	24
	HIMANSHU SHARMA	15/151403	15048567038	III	VI	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	25	23
34	RAHUL SHARMA	15/151404	15048567039	III	VI	32221601	ELECTROMAGNETIC THEORY	25	21
	RAHUL SHARMA	15/151404	15048567039	III	VI	32221602	STATISTICAL MECHANICS	25	22
	RAHUL SHARMA	15/151404	15048567039	III	VI	32227613	COMMUNICATION SYSTEM	25	22
	RAHUL SHARMA	15/151404	15048567039	III	VI	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	25	20
35	DHARMESH KUMAR	15/151405	15048567040	III	VI	32221601	ELECTROMAGNETIC THEORY	25	16

	DHARMESH KUMAR	15/151405	15048567040	III	VI	32221602	STATISTICAL MECHANICS	25	19
	DHARMESH KUMAR	15/151405	15048567040	III	VI	32227613	COMMUNICATION SYSTEM	25	17
	DHARMESH KUMAR	15/151405	15048567040	III	VI	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	25	20
36	HITESH AGGARWAL	15/151406	15048567041	III	VI	32221601	ELECTROMAGNETIC THEORY	25	21
	HITESH AGGARWAL	15/151406	15048567041	III	VI	32221602	STATISTICAL MECHANICS	25	22
	HITESH AGGARWAL	15/151406	15048567041	III	VI	32227613	COMMUNICATION SYSTEM	25	24
	HITESH AGGARWAL	15/151406	15048567041	III	VI	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	25	22
37	MANISH KUMAR SINGH	15/151407	15048567042	III	VI	32221601	ELECTROMAGNETIC THEORY	25	19
	MANISH KUMAR SINGH	15/151407	15048567042	III	VI	32221602	STATISTICAL MECHANICS	25	22
	MANISH KUMAR SINGH	15/151407	15048567042	III	VI	32227613	COMMUNICATION SYSTEM	25	18
	MANISH KUMAR SINGH	15/151407	15048567042	III	VI	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	25	20
38	ROHIT RAWAT	15/151409	15048567043	III	VI	32221601	ELECTROMAGNETIC THEORY	25	21
	ROHIT RAWAT	15/151409	15048567043	III	VI	32221602	STATISTICAL MECHANICS	25	22
	ROHIT RAWAT	15/151409	15048567043	III	VI	32227613	COMMUNICATION SYSTEM	25	22
	ROHIT RAWAT	15/151409	15048567043	III	VI	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	25	21
39	SAJAL RAI	15/151411	15048567044	III	VI	32221601	ELECTROMAGNETIC THEORY	25	15
	SAJAL RAI	15/151411	15048567044	III	VI	32221602	STATISTICAL MECHANICS	25	16
	SAJAL RAI	15/151411	15048567044	III	VI	32227613	COMMUNICATION SYSTEM	25	17
	SAJAL RAI	15/151411	15048567044	III	VI	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	25	18
40	SUCHIR SUNTHA	15/151412	15048567045	III	VI	32221601	ELECTROMAGNETIC THEORY	25	22
	SUCHIR SUNTHA	15/151412	15048567045	III	VI	32221602	STATISTICAL MECHANICS	25	23
	SUCHIR SUNTHA	15/151412	15048567045	III	VI	32227613	COMMUNICATION SYSTEM	25	21

	SUCHIR SUNTHA	15/151412	15048567045	III	VI	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	25	21
41	ASHISH KUMAR	15/151413	15048567046	III	VI	32221601	ELECTROMAGNETIC THEORY	25	24
	ASHISH KUMAR	15/151413	15048567046	III	VI	32221602	STATISTICAL MECHANICS	25	24
	ASHISH KUMAR	15/151413	15048567046	III	VI	32227613	COMMUNICATION SYSTEM	25	22
	ASHISH KUMAR	15/151413	15048567046	III	VI	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	25	20
42	HAZRAT KHAJA ALI	15/151414	15048567047	III	VI	32221601	ELECTROMAGNETIC THEORY	25	17
	HAZRAT KHAJA ALI	15/151414	15048567047	III	VI	32221602	STATISTICAL MECHANICS	25	18
	HAZRAT KHAJA ALI	15/151414	15048567047	III	VI	32227613	COMMUNICATION SYSTEM	25	19
	HAZRAT KHAJA ALI	15/151414	15048567047	III	VI	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	25	16
43	KUMUD RANJAN JHA	15/151418	15048567048	III	VI	32221601	ELECTROMAGNETIC THEORY	25	15
	KUMUD RANJAN JHA	15/151418	15048567048	III	VI	32221602	STATISTICAL MECHANICS	25	23
	KUMUD RANJAN JHA	15/151418	15048567048	III	VI	32227613	COMMUNICATION SYSTEM	25	18
	KUMUD RANJAN JHA	15/151418	15048567048	III	VI	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	25	18
44	PRERNA MALIK	15/151420	15048567049	III	VI	32221601	ELECTROMAGNETIC THEORY	25	14
	PRERNA MALIK	15/151420	15048567049	III	VI	32221602	STATISTICAL MECHANICS	25	18
	PRERNA MALIK	15/151420	15048567049	III	VI	32227613	COMMUNICATION SYSTEM	25	18
	PRERNA MALIK	15/151420	15048567049	III	VI	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	25	15
45	DHARMENDER MEENA	15/151421	15048567050	III	VI	32221601	ELECTROMAGNETIC THEORY	25	23
	DHARMENDER MEENA	15/151421	15048567050	III	VI	32221602	STATISTICAL MECHANICS	25	21
	DHARMENDER MEENA	15/151421	15048567050	III	VI	32227613	COMMUNICATION SYSTEM	25	24
	DHARMENDER MEENA	15/151421	15048567050	III	VI	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	25	19
46	VISHAL GUPTA	15/151422	15048567051	III	VI	32221601	ELECTROMAGNETIC THEORY	25	20
	VISHAL GUPTA	15/151422	15048567051	III	VI	32221602	STATISTICAL MECHANICS	25	20

	VISHAL GUPTA	15/151422	15048567051	III	VI	32227613	COMMUNICATION SYSTEM	25	24	
	VISHAL GUPTA	15/151422	15048567051	III	VI	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	25	18	